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**computer organization & architecture**

Lab Report no. 4

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17l-4497

EL-B2

**LAB Report no. 4**

**Name:** Rayan Tahir Khan

**Roll no.:** 17L-4497

**Section:** EE-B **Lab:** EL-B2

SIMULATING CACHE USING MIPS SIMULATOR

**Introduction:**

In this lab we used a MIPS simulator to understand the role and types of cache, its effects on the performance and its working.

**Objective:**

* To be able to understand working of cache
* To learn the use of MIPS simulator.

**Design/Procedure:**

We had done 3 exercise in this lab

Conclusion:

Application:

**POST LAB**